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EXAMINATION: Circuit Theory and Introductory Electronics	EXAMINER: W Ens

Answer all questions. All questions except 7 are worth 10 marks. Number 7 is worth 20 marks.

1. For the circuit shown below, find the value of V_3 such that the current in the 10 Ω resistor is zero.



2. Find the Thevenin equivalent circuit for the circuit shown below.



3. In the RC circuit shown below, determine the output voltage as a function of time if the input is stepped from zero to V at time t = 0. What is the current at very long times?



4. For the circuit of question 3, determine the output voltage if the input is given by $v_{in} = V \cos \omega t$. What is the phase shift? Is this a high-pass or a low-pass filter? What is the breakpoint frequency, where the gain drops by 3 dB from its maximum value?

5. For the circuit of question 3 with $R = 1 \text{ k}\Omega$ and $C = 0.10 \mu\text{F}$, determine what load connected to the output gives maximum power transfer, if the input voltage is sinusoidal with a frequency of 1000 Hz.

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6. In the rectifier circuit shown below, the input voltage is sinusoidal with a peak voltage of 100 V and a frequency of 50 Hz. Sketch the output voltage (across R_L) for (a) no capacitor, (b) $C = 10 \ \mu$ F, and (c) $C = 100 \ \mu$ F. Overlay the 3 outputs on the same axes. Calculate the peak-to-peak ripple for (c). Treat the diode as an ideal diode.



7. The common emitter amplifier shown below is configured to give the load line and operating point as indicated on the characteristic curves.

- (a) Select values of V_{cc} , R_c , R_E , R_I , and R_2 that would give this load line and operating point and a gain of -6.
- (b) What value of C_1 would cause the gain to drop by 3 dB at 100 Hz, ignoring other contributions to the attenuation at this frequency? Assume $r_{BE} = 2 \text{ k}\Omega$.



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8. Show that the output of the circuit below is approximately propertional to the derivative of the		

8. Show that the output of the circuit below is approximately proportional to the derivative of the input, and give the condition for the validity of the approximation.



Draw the circuit for a passive differentiator, and give the condition for its validity.

9. Find an expression for the output voltage of the circuit shown below in terms of the 4 input voltages and the resistance R. Simplify the result as much as possible.



10. Briefly explain the function of the op amp in a band pass filter like the one shown below.



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11. Using diodes, npn transistors, and resistors, give an example of a circuit diagram for 3 of the following gates: (a) OR, (b) NOR, (c) AND, (d) NAND, and (e) NOT. (3 diagrams are expected)

12. A half-adder takes two inputs (A, B) and provides two outputs $(A \oplus B, A \cdot B)$. Show how to implement a half adder using only NOR gates.

13. (a) In the NAND RS flip flop below, what is the relationship between B and Y? (b) In the NOR RS flip flop below, what is the relationship between B and Y?



The End

Phys 2610 (2017) Final Learn solution $I_3 = 0 \implies I_1 = I_2 = I$ Then KVL around outer loop: 30V - I (92+62)-15V=0 \rightarrow I = $\frac{15V}{15r} = 1A$. KVL on left side: $30V - I92 - V_3 = 0$ \rightarrow V3 = 30V - 9V = 21V 2) Therein resistance from 600 \$ 600 \$ 300 Rth: 300 2 // (300 2 + 600 2 //6002) = 300x // (300x + 300x) = 300x // 600x = 200x Therein voltage = open cet voltage, Redraving $I = \frac{10^{10}}{10^{10}}$ $I = \frac{10^{10}}{10^{10}}$ = 10V 90052 = .0111A. Then $V_1 = 10V - I(600 x) = 3'/_3 V \longrightarrow V = \frac{V_1}{2} = 1.67 V$

KVL : V- 8/c - iR= 0 $\rightarrow \frac{di}{dt} + \frac{1}{RC} i = 0$ Solution is: i = Ae^{-t/RC} At t=0, No=0, so iR=V => A=V => i= V -t/re Then the output is No=iR= Ve-t/RC Fortan, i->0 The complex impedance is (4) $Z = R + \frac{1}{j\omega c} = |Z| e^{j\varphi} \quad \text{with } |Z| = \sqrt{R^2 + ('/\omega c)^2}$ and $\tan \varphi = (1/\omega Rc)$ So the current is (the real part of): i = <u>Nin = V</u> e j(wt-d) Z |Z| and the output voltage is (the real part of): $N_{out} = iR = \frac{V}{\sqrt{1 + (1/w_{RC})^2}} e^{j(wt - \varphi)}$ The phase shift is -d= - arctan (1/wRC) AL W = 00, Nout = Nin, so this is a high pass filter At the breakpoint freq. WB, Nout -> Nin/JZ => VI+('lwre)2' -> JZ' => WB -> 1/RC

(c)
$$V_{act} = R_{L}$$
 $V_{b} = V_{b} c_{b} \omega t$
 $V_{p} = 100V$
 $W = 2\pi (50Hz)$
 $V_{p} = 00V$
 $W = 2\pi (50Hz)$
 $V_{p} = 100V$
 $W = 2\pi (50Hz)$
 $V_{p} = 100V$
 $V_{p} = 100V$
 $V_{p} = 100V$
 $V_{p} = 10\mu F$: $V_{p} = 10\mu F = 0.010S = 10\mu s$
 $V_{out} = 100V e$
 $V_{out} (t' = 20 ms) = 82V$
 $V_{avgule} = V_{p} - 82V = 18V$
 $Alvo V_{p} = Nout (t'=0) - Nout (t'-T)$
 $\cong V_{p} (1 - e^{-Tht}) \equiv V_{pT} = 20V$

$$\begin{array}{c} \textcircledleft \\ \textcircledleft \\ \hline \textcircledleft \\ \hline \fboxleft \\ \hline \fboxleft \\ \hline \reft \\ \hline$$

Jo find the attenuation by C, , consider it in series with the ac equivalent criticut for the amplifier Nin C, FR, FR FE FRC (7) (6) The input resistance of the amplifies forme a high pass filter with C .: Nin C, ZRin Nin For the above circuit Rin = R. 1/R2/ (JBE+BRE) = R. //R2= 10.3 k 52 Attenuation by 3 dB occurs when w = I Rin C. $L_1 = U_B R in = 2\pi (100 Hz) (10.3 kz)$ C. = 154 mF

$$\begin{array}{c} \end{array}$$

8 N, and J may 9 Since current into op amp is negligible, i, tiz = ia and istiy = ib $\frac{N_1 - N_2 + N_2 - N_2}{R} = \frac{N_2 - N_{out}}{R} \longrightarrow N_1 + N_2 = 3N_2 - N_{out}$ $\frac{N_3 - N_7 + N_4 - N_7}{R} = \frac{N_7}{R} \longrightarrow N_3 + N_4 = 3N_7$ and Using No = N_, and taking the difference, $Nout = N_3 + N_4 - (v_1 + N_2)$ (10)The op amp acts as a buffer, preventing the second filter from affecting the performance

of the first, which is usually calculated

without a load. The op amp passes the voltage

signal (usually with unity gain), but presents no load (infinite impedance) to the first filter.







$$=$$
 $Y = B + 0 = B$