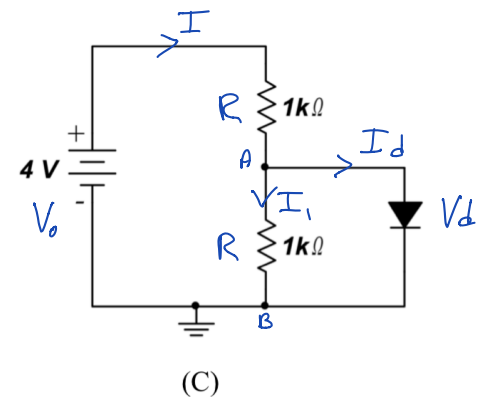
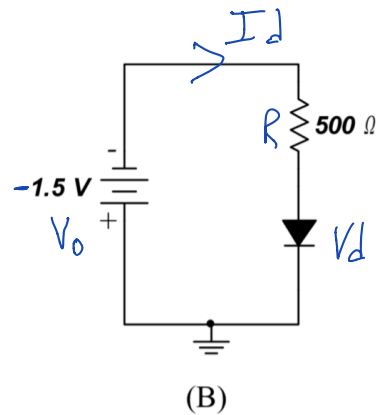
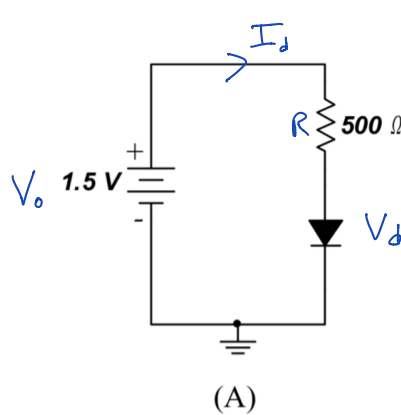
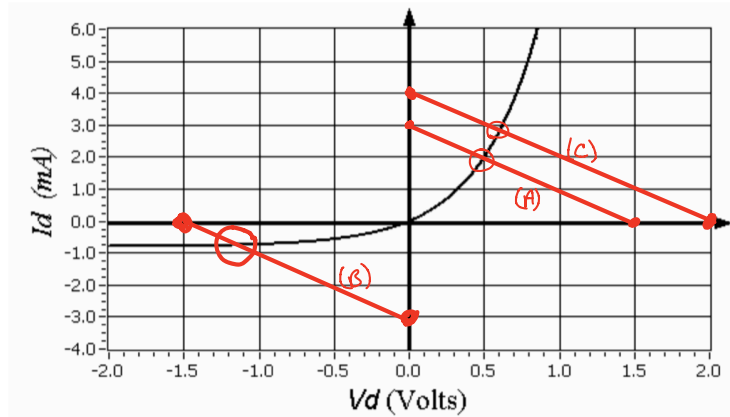
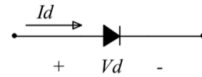


Phys2610 (2019) Assignment 4 solutions

1. The graph below shows the I - V characteristic curve of a certain diode. The diode is used in the following circuits. Draw on the plot and label, the load-line you would use for each circuit in order to obtain the diode voltage and current. Do not use an equivalent circuit for the diodes for this purpose. You should get an equation for I_d as a function of V_d . Is the result approximately consistent with the result you expect from replacing the diodes with a suitable equivalent model?

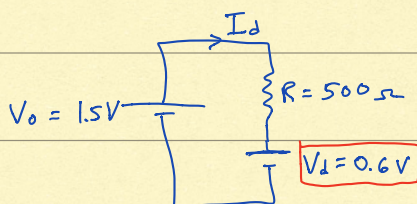


A) $V_0 - I_d R - V_d = 0 \rightarrow I_d = \frac{V_0}{R} - \frac{V_d}{R}$ or $I_d = 3.0 \text{ mA} - \frac{V_d}{500 \Omega}$

\rightarrow line connecting points $(V_d, I_d) = (V_0, 0)$ and $(0, \frac{V_0}{R})$ or $(1.5 \text{ V}, 0) + (0, 3 \text{ mA})$

\rightarrow op. point: $V_d = 0.5 \text{ V}, I_d = 2 \text{ mA}$ from load line analysis

The diode is forward biased (on) so a simple equivalent circuit is



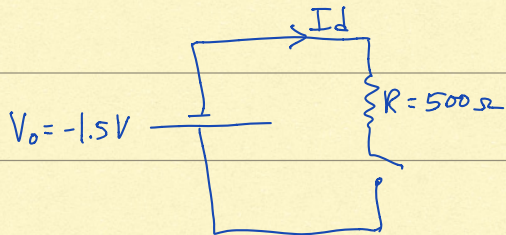
$\Rightarrow I_d = \frac{V_0 - V_d}{R} = 1.8 \text{ mA}$ from eq. ckt. analysis

$$B) V_0 - I_d R - V_d = 0 \rightarrow I_d = \frac{V_0}{R} - \frac{V_d}{R} \quad \text{or} \quad \boxed{I_d = -3.0 \text{ mA} - \frac{V_d}{500 \Omega}}$$

Here, $V_0 = -1.5 \text{ V}$, so the line connects $(-1.5 \text{ V}, 0) + (0, -3 \text{ mA})$

$$\rightarrow \text{op. point: } \boxed{V_d = -1.2 \text{ V}, I_d = -0.7 \text{ mA}} \quad \text{load line}$$

The diode is reverse biased, so a simple equivalent circuit is open switch:



$$\therefore \boxed{I_d = 0 \text{ and } V_d = -1.5 \text{ V}} \quad \text{eq. ckt.}$$

$$C) \text{ Applying KVL on the outer loop: } V_0 - IR - V_d = 0$$

$$\text{and KCL at point A: } I = I_d + I_1 = I_d + \frac{V_d}{R} \quad \left(\text{using } I_1 = \frac{V_d}{R} \right)$$

$$\text{Eliminate } I: V_0 - \left(I_d + \frac{V_d}{R} \right) R - V_d = 0$$

$$\text{Solve for } I_d: I_d = \frac{V_0}{R} - \frac{2V_d}{R} \quad \text{or} \quad \boxed{I_d = 4 \text{ mA} - \frac{V_d}{500 \Omega}}$$

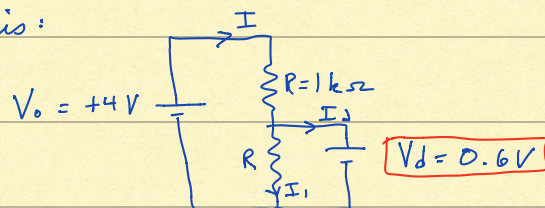
$$\rightarrow \text{line between } (V_d, I_d) = \left(\frac{V_0}{2}, 0 \right) + \left(0, \frac{V_0}{R} \right) \quad \text{or} \quad (2 \text{ V}, 0) + (0, 4 \text{ mA})$$

$$\rightarrow \text{op. point: } \boxed{V_d = 0.6 \text{ V}, I_d = 2.8 \text{ mA}} \quad \text{load line}$$

If the diode were not there, the voltage between A + B would be $+2 \text{ V}$, so

with the diode in place, it is turned on. So a simple

equivalent circuit is:



$$\therefore I = \frac{V_0 - V_d}{R} = \frac{3.4 \text{ V}}{1 \text{ k}\Omega} = 3.4 \text{ mA} \quad \left. \begin{array}{l} \\ \\ \end{array} \right\} I_d = I - I_1 = \boxed{2.8 \text{ mA}} \quad \text{eq. ckt.}$$

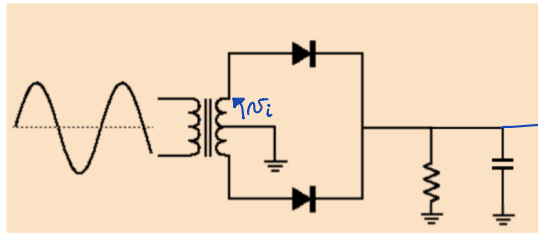
$$I_1 = \frac{V_d}{R} = 0.6 \text{ mA}$$

2. Calculate the approximate peak-to-peak ripple and the ripple factor for a centre-tapped full-wave rectifier. The two outer taps of the secondary coil have a peak voltage of 170 V with respect to the centre tap, and the frequency is 60 Hz. The output is filtered using a 3000 μF capacitor and the load resistance is 200 Ω .

$$N_i = V_o \sin \omega t$$

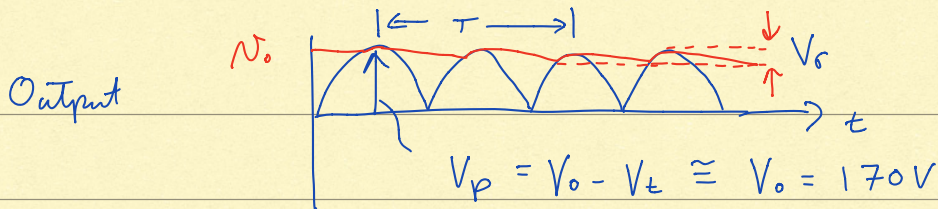
$$V_o = 170 \text{ V}$$

$$\omega = 2\pi (60 \text{ Hz})$$



$$R = 200 \Omega$$

$$C = 3000 \mu\text{F}$$



Time constant for decay of output is $\tau = RC = (200 \Omega)(3000 \mu\text{F}) = 0.6 \text{ s}$

Period of the input: $T = 1/f = 1/60 \text{ Hz} = 16.7 \text{ ms}$

Output period: $T_o = T/2 = \frac{1}{2f} = 8.33 \text{ ms}$

Output voltage = $v_o = V_p e^{-t/\tau} \cong V_p (1 - T_o/\tau)$ (because $T_o \ll \tau$)

Amplitude of ripple: $V_r = v_o(t=0) - v_o(t=T_o) = V_p - V_p(1 - T_o/\tau) = \frac{V_p T_o}{\tau}$

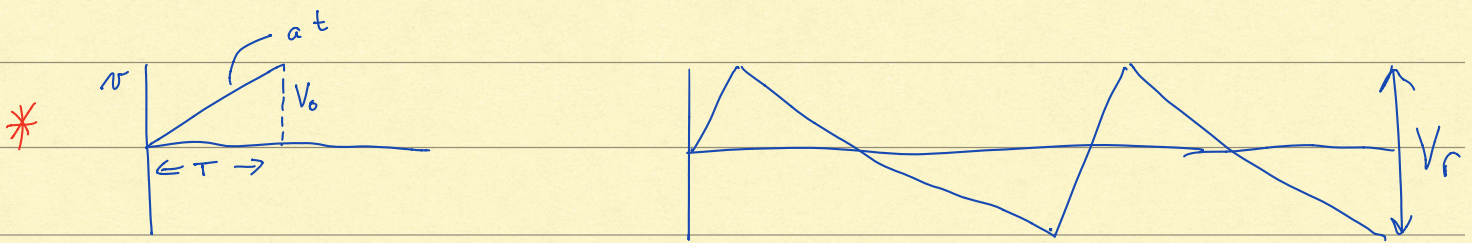
$$= \frac{V_p}{2fRC} = \frac{170 \text{ V}}{2(60 \text{ Hz})(0.6 \text{ s})} = 2.36 \text{ V}$$

Ripple factor: $\mathcal{R} = \frac{V_{\text{rms}}(\text{ac})}{V_{\text{dc}}}$

Approximating the output by a triangular wave with peak-to-peak amplitude equal to $V_r = 2.35 \text{ V}$, the rms voltage is $\frac{V_r}{2\sqrt{3}} = \frac{2.36 \text{ V}}{2\sqrt{3}} = 0.68 \text{ V}$

The dc average voltage is $V_{\text{dc}} = V_p - \frac{V_r}{2} = 170 \text{ V} - \frac{2.35 \text{ V}}{2} = 169 \text{ V}$ ($\cong V_p$)

$$\therefore \mathcal{R} = \frac{V_{\text{rms}}}{V_{\text{dc}}} = \frac{0.68}{169} = \boxed{0.0040}$$



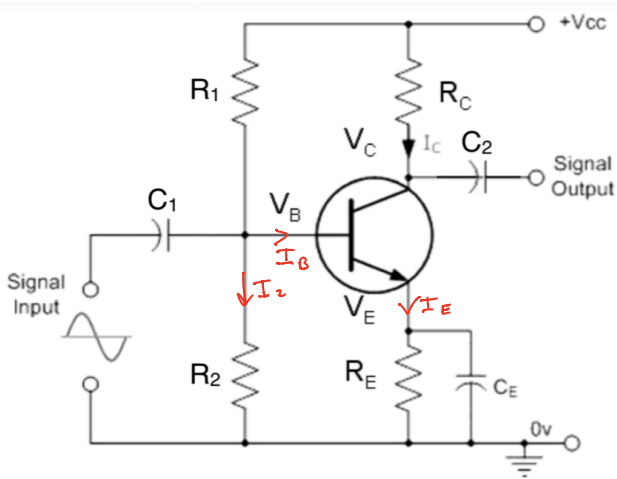
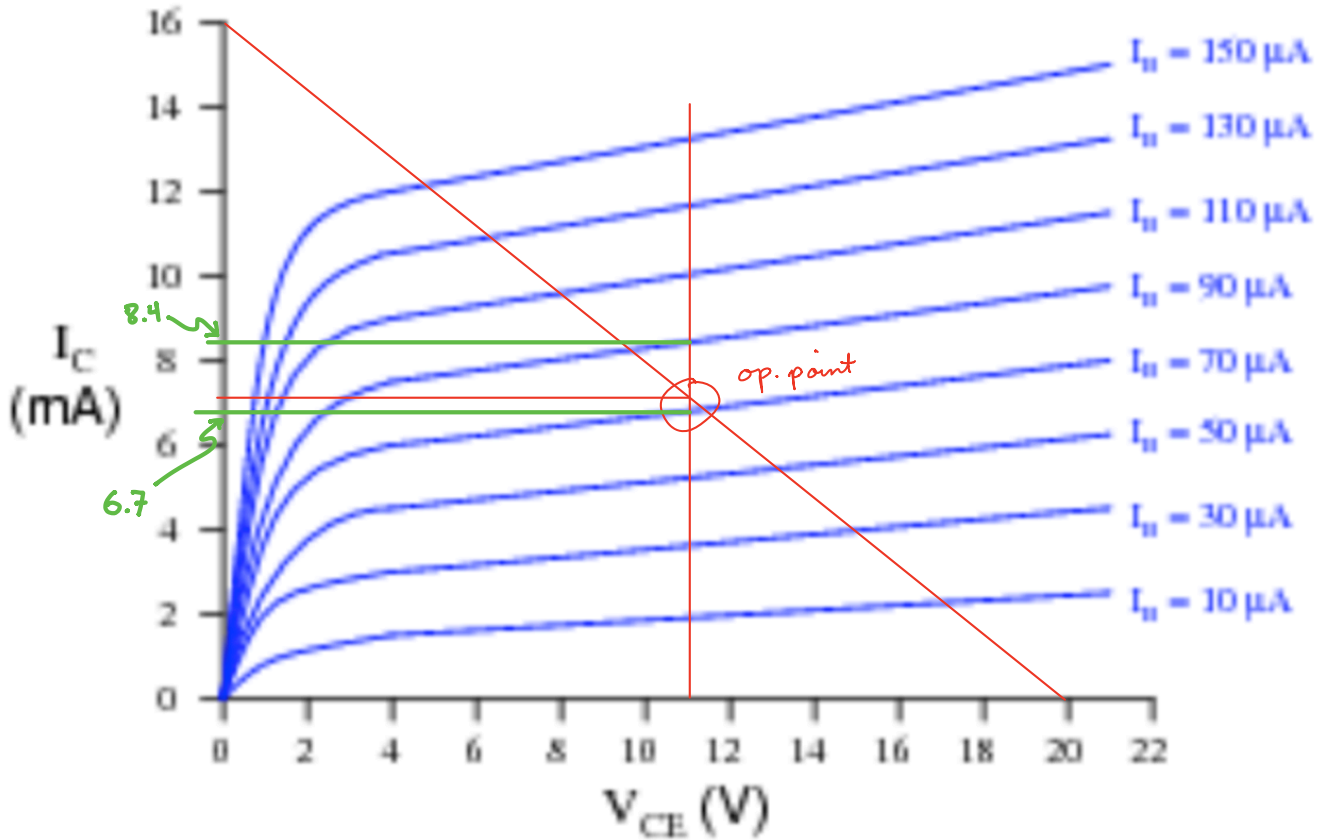
For any linear voltage with peak V_0 , the rms value is

$$V_{rms} = \left[\frac{1}{T} \int_0^T v^2 dt \right]^{1/2} = \left[\frac{1}{T} a^2 \frac{t^3}{3} \Big|_0^T \right]^{1/2}$$

$$= \sqrt{\frac{a^2 T^2}{3}} = \frac{aT}{\sqrt{3}} = \frac{V_0}{\sqrt{3}}$$

Therefore, for connected segments with peak-to-peak voltage $V_r = 2V_0$,
the rms voltage is $V_{rms} = \frac{V_r}{2\sqrt{3}}$

3. Design an H-biased common emitter amplifier circuit that will set a reasonable operating point for a transistor with the characteristics shown below.



(a) Select a load line to give V_{cc} and $R_c + R_E$:

$$\text{load line: } I_c = \frac{V_{cc}}{R_c + R_E} - \frac{V_{CE}}{R_c + R_E}$$

From the graph,

$$\text{for } I_c = 0, V_{CE} = V_{cc} = 20V$$

$$\text{and for } V_{CE} = 0, I_c = 16mA \rightarrow R_c + R_E = 1.25k\Omega$$

(b) Choose $R_E = 250\Omega$ to give $R_c = 1k\Omega$

(c) Select an operating point to give R_1 and R_2 :

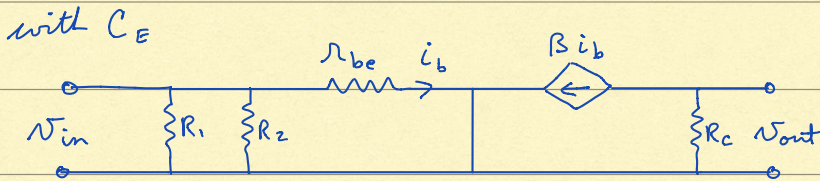
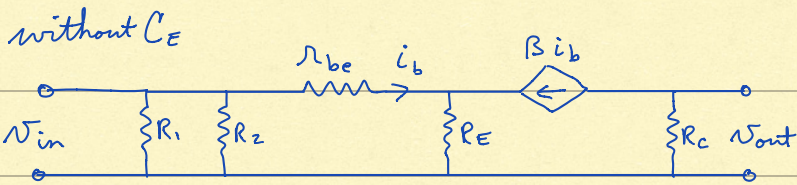
$$\text{From the graph, at the op. pt., } I_B = 73\mu A, I_c = 7.1mA, V_{CE} = 11V \Rightarrow V_E = I_E R_E \approx I_c R_c = 1.78V$$

$$\text{Then } V_B = V_{BE} + V_E = 0.6V + 1.78V = 2.38V$$

$$\text{Choose } I_Z = 20 I_B = 1.46mA \Rightarrow R_2 = \frac{V_B}{I_Z} = 1.63k\Omega$$

$$\text{Then } R_1 = \frac{V_{cc} - V_B}{I_Z} = 12.1k\Omega$$

4. Draw a simplified ac equivalent circuit for the circuit of question 3, and estimate the midband voltage gain and input and output impedances with and without a bypass capacitor across the emitter resistor. Use $r_{BE} = 2 \text{ k}\Omega$.



From problem 3, $R_1 = 12.1 \text{ k}\Omega$, $R_2 = 1.63 \text{ k}\Omega$, $R_C = 1 \text{ k}\Omega$, $R_E = 250 \Omega$

(a) Voltage gain without C_E :

$$a = \frac{V_{out}}{V_{in}} = \frac{-\beta i_b R_C}{i_b r_{be} + \beta i_b R_E} = \frac{-\beta R_C}{r_{be} + \beta R_E}$$

From the graph, $\beta_{ac} \approx \frac{(8.4 - 6.7) \text{ mA}}{20 \mu\text{A}} = 85$, so $a = -3.6$

or using $a \approx -R_C/R_E = -4$

(b) voltage gain with C_E : $a = \frac{-\beta R_C}{r_{be}} = -42.5$

(c) input impedance without C_E : $R_{in} = R_1 // R_2 // (r_{be} + \beta R_E) = 1.42 \text{ k}\Omega$

$$\approx R_1 // R_2 = 1.44 \text{ k}\Omega$$

(d) input impedance with C_E : $R_{in} = R_1 // R_2 // r_{be} = 840 \Omega$

(e) output impedance: $R_{out} = \frac{V_{out}(\text{open})}{I_{out}(\text{short})} = \frac{\beta i_b R_C}{\beta i_b} = R_C = 1.0 \text{ k}\Omega$